

<b>Notic of References Cit d</b>	Application/Control No. 10/056,287		Applicant(s)/Patent Under Reexamination WEST, BURNELL G.	
	Examiner John H Le		Art Unit 2863	Page 1 of 1

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	U	Boerker, "Data reception circuit", Pub. No.: US 2003/0035502, PubDate: February 20, 2003, Foreign Application Priority filed on January 15, 2001.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.